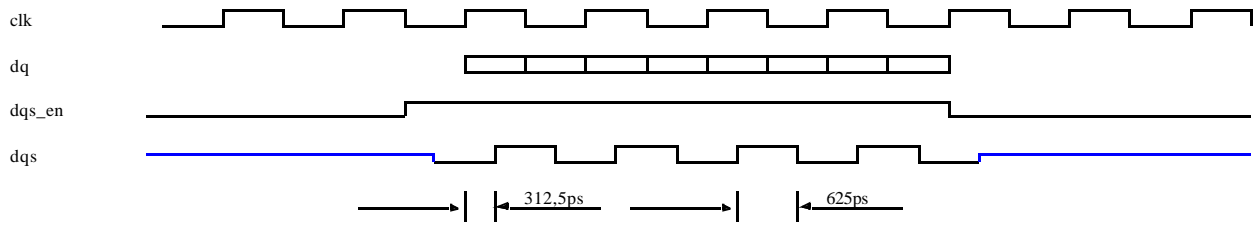
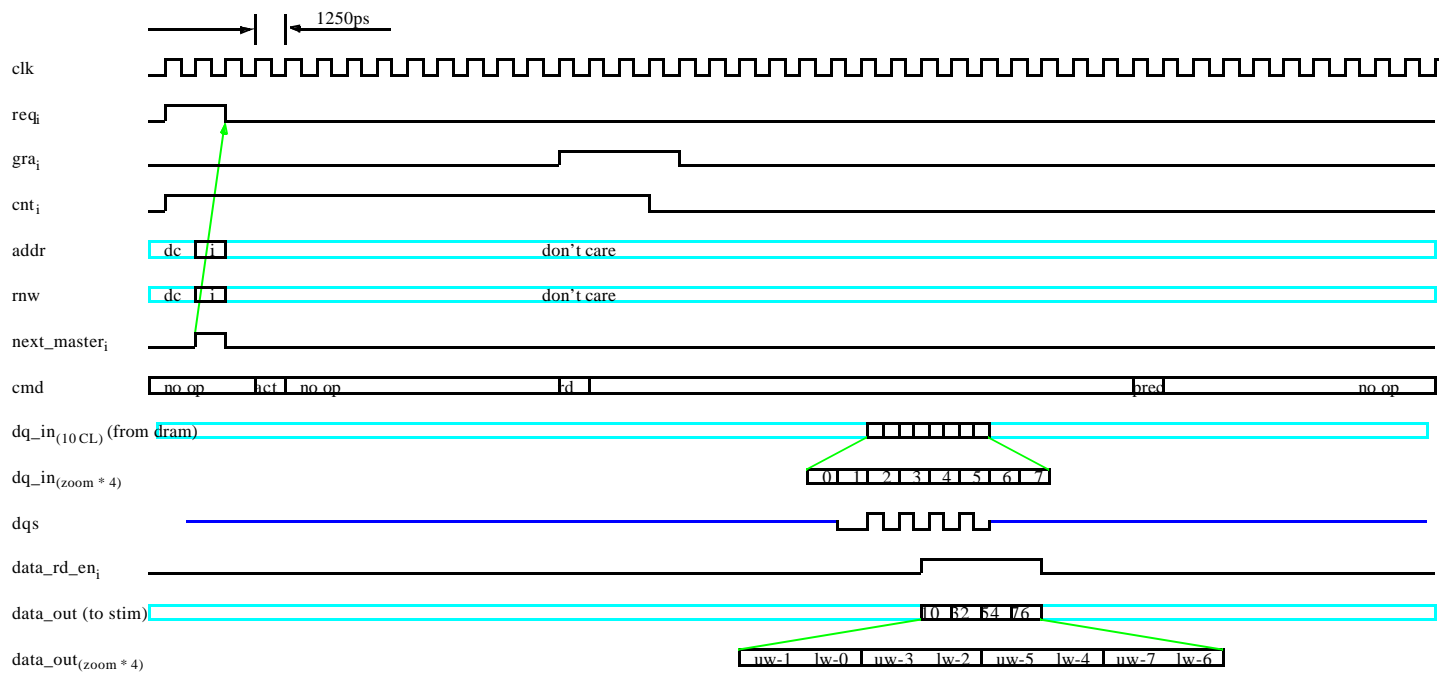
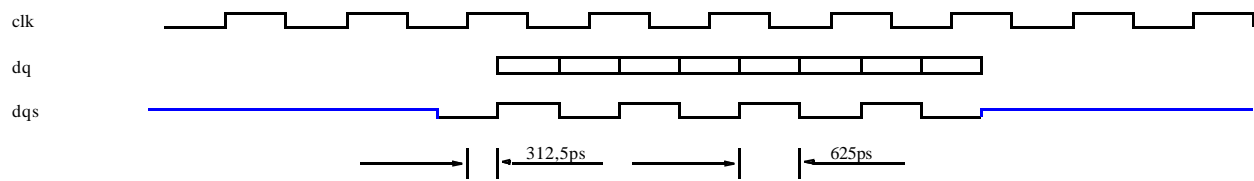


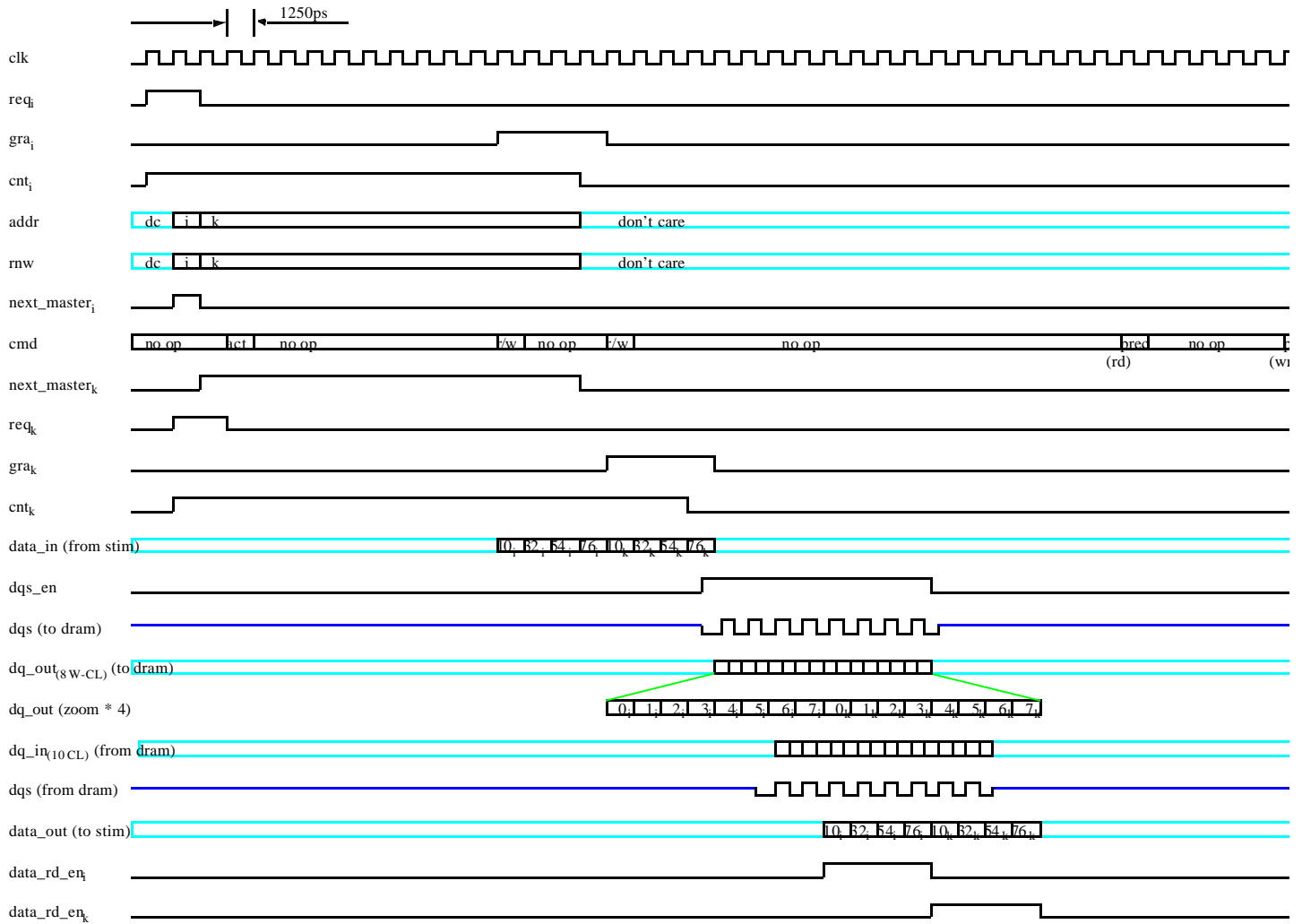
ddr3; wr





ddr3; rd





ddr3; fast wr2wr & rd2rd