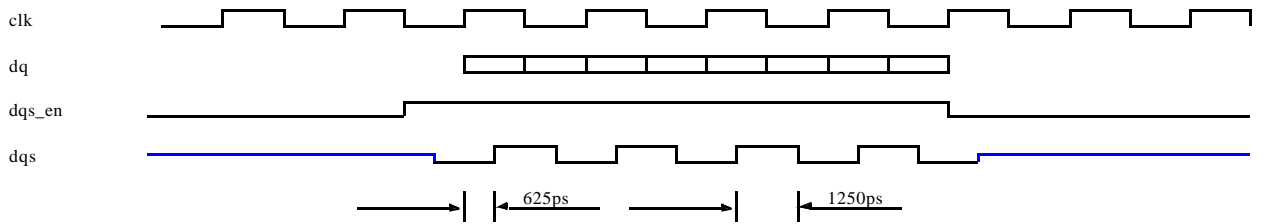
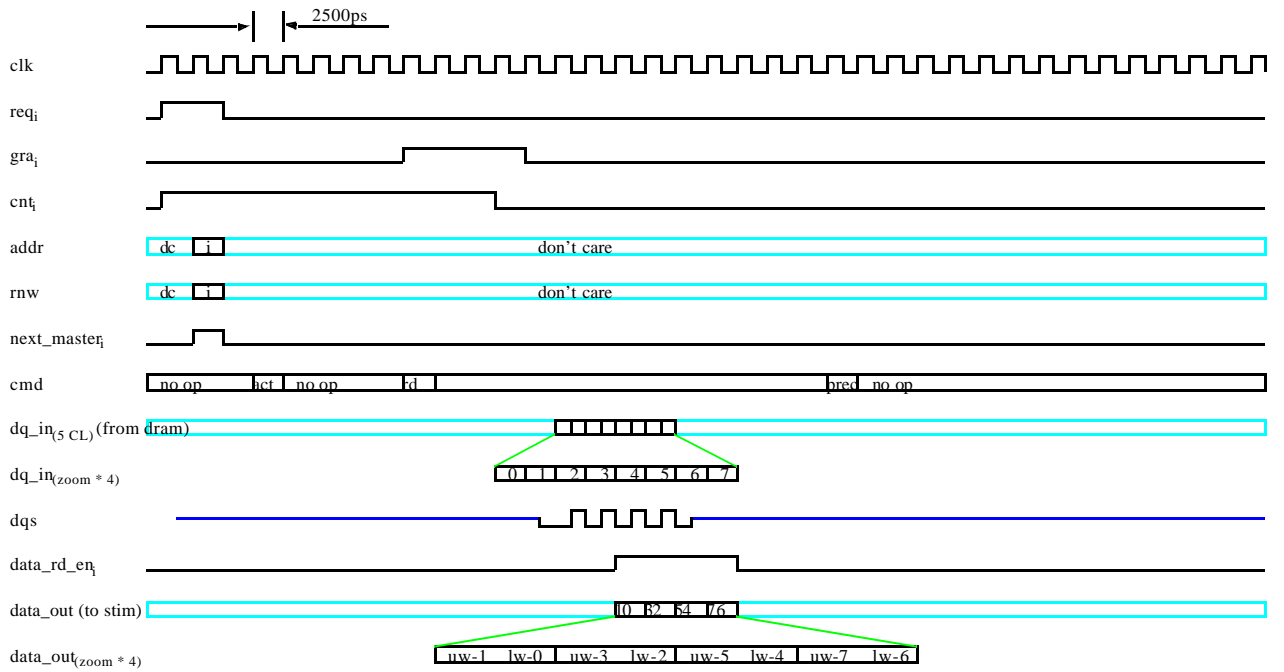
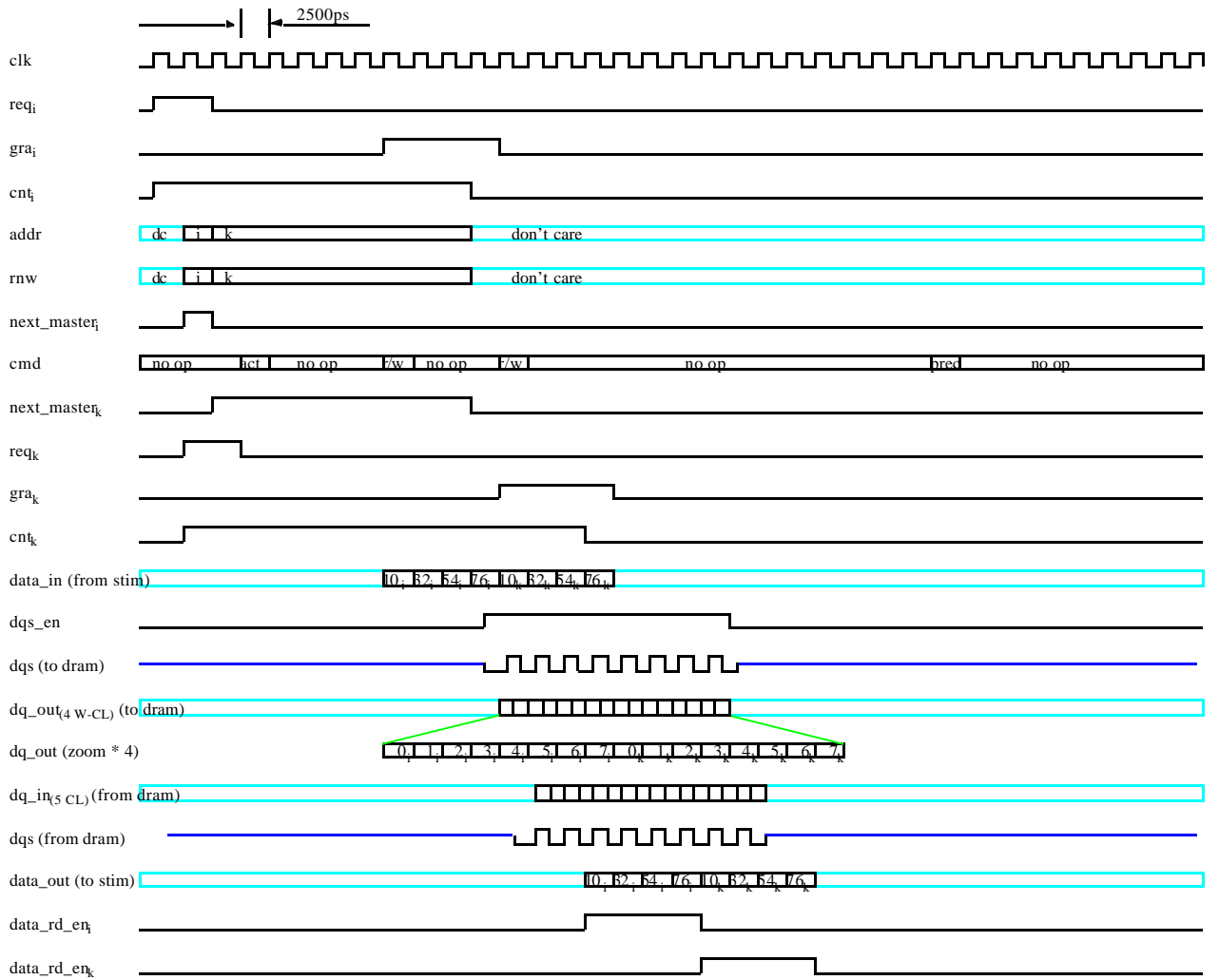


ddr2; wr





ddr2; rd



ddr2; fast wr2wr & rd2rd